

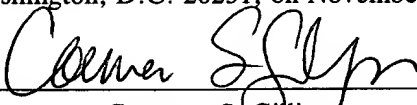
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Docket: 0756-1724

GP2814/18
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VJG

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on November 24, 1999.


Cameron S. Gilligan

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Patent Application of)

DEC 01 1999

Masaaki HIROKI et al.)

TECHNOLOGY CENTER 2800

Serial No. 08/943,238)

Group Art Unit: 2814

Filed: October 3, 1997)

Examiner: S. Rao

For: ELECTRO-OPTICAL DEVICE)

Date: November 24, 1999

INFORMATION DISCLOSURE STATEMENT

AND CORRECTION OF PRIOR

INFORMATION DISCLOSURE STATEMENT

AND NOTIFICATION OF RELATED APPLICATIONS

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of record in the above-identified application.

JP 49-52598 is a counterpart application of U.S. Patent No. 3,838,209, and GB 1 396 486.

JP 59-61818 is a counterpart application of GB 2 130 781 and U.S. Patent Nos. 4,600,274; 4,653,862; 4,716,403; RE33,882.

With respect to Japanese Utility Model No. 60-154996, Applicants could not obtain an English abstract or equivalent English language patents thereof. This reference is directed to a driving circuit of a display having a capacitive load such as liquid crystal display. The driving means supplies a display device with a scan signal and a gradational signal which is superimposed on the scan signal within a scanning period based on an off period of the scan signal. The Japanese Patent Office appears to assert that it is well known to change a period of writing signal data to a pixel within a unit period based on a frequency having a periodicity which is obtained by dividing the unit period.

In supplement to the Information Disclosure Statement filed October 3, 1997, submitted herewith is a corrected Form PTO-1449 to be made of record in the above-identified application. Upon further review of the application, the undersigned noticed several typographical errors contained in the IDS. For U.S. Patent No. 4,752,774, the first inventor's name should be listed as Clerc et al. and for U.S. Patent No. 5,084,905, the first inventor's name should be listed as Sasaki et al. Further, Japanese Application No. 1-068724 was cited incorrectly and should have been JP 64-068734. Lastly, JP 63-10777 is incorrect and should read as JP 63-100777.

Pursuant to Applicant's duty of candor under 37 C.F.R. §1.56, Applicant wishes to inform the Examiner of the following applications claiming related technical subject matter:


<u>Serial No.</u>	<u>Filing Date</u>	<u>Status</u>
08/943,333	October 1, 1997	Pending
09/285,899	April 8, 1999	Pending
09/288,140	April 8, 1999	Pending
09/288,509	April 8, 1999	Pending
09/300,596	April 28, 1999	Pending
09/315,987	May 21, 1999	Pending
09/316,198	May 21, 1999	Pending

Submitted herewith is the requisite fee of \$240.00. Hence, it is requested that the references submitted herewith be considered and made of record in the subject application. A duplicate of this paper is submitted herewith.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380.

A copy of the references cited herein is submitted herewith in accordance with 37 C.F.R. 1.98(a).

Respectfully submitted,



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